Transformerless H-bridge inverter based PV system for harmonic current compensation.

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Abstract—This paper presents a transformer less hybrid series active filter (THSeAF) based PV system. The proposed system to enhance the power quality in single-phase systems with constant supply, which supports critical loads behaviors as highharmonic impedance. In this method, deals with energy management and power quality issues related to electric transportation and focus on improving the electric vehicle load connected to grid. This is based on the Power Factor Correction (PFC) conversion with harmonic modulation technique that will give betterment of power factor in PFC operation. The control strategy was designed to prevention of current harmonic distortions with the nonlinear loads to control the flow of utility without any usual bulky and costly transformer. Power factor along with AC side will also maintained to some value and also eliminate the voltage distortions at the Common coupling point. The proposed system protecting sensitive loads from voltage distortions, swells and sags with respect to power system, without the series transformer it is advantageous for an industrial implementation. This paper carried out with 2-kVA power demonstrating the effectiveness of the existing topology.

Keywords—Power factor correction(PFC), Dynamic voltage restore(DVR), Transformerless hybrid series active filter(THSeAF), Series active filter(SeAF), Source current harmonics, Voltage distortion, power quality improvement.

1. INTRODUCTION

The forecast of future smart grid associated with electric vehicles charging stations leads to serious concerns for power quality in power distribution system. The harmonics generated by non-linear loads (electric vehicle batteries) indeed have a great impact on the system which effects the equipment should be considered in modern grid. The increased current distorts heat, losses and causes failure of equipment. This phenomenon must be eliminated, which reduces the efficiency of the system [1].

To prevent voltage distortion at the point of common coupling PCC, Dynamic Voltage Restorer is advised. Another way to protect the system is to directly reduce the pollution of power electronics based loads at their source. There are other attempts to protect the plant. There exist two active devices to overcome described power quality issues [2]. The first category is series active filter Series Active Filter (SeAF) including hybrid, can reduce current harmonics fed from nonlinear loads. Shunt active filters are well known compared to series active filters. But there is a great advantage of series active filters that is low level compensators can be used. Howe ever, the complexion configuration and necessity of isolation of series transformer makes in an industrial application [3]. The second category is to compensate voltage issues. A Dynamic Voltage Restore (DVR) is same as SeAF. These two categories are different from each other in control principles. The difference depends on the purpose of the system.



Fig.1 Proposed Electrical diagram of the THSeAF in a single phase utility.

The advantage of the proposed system is it can compensate the harmonic voltage and currents produced by non-linear loads [4]. Transformer less Hybrid Series Active Filter is an alternate option to the conventional power transferring converts, where each phase can be controlled and operated without disturbing other phases [5]. This paper demonstrates that the separation of three phase converter to a single phase H-bridge converter has allowed the elimination of transformer cost by isolating transformer and encourages industrial application for filtering purposes. The setup has shown great effectiveness in the requested performance in solving the problems of current and voltage distortions, power factor PF on load terminals. The overall block diagram as shown in Fig.1

Symbol	Definition	Value
v_s	Line phase-to-neutral voltage	120 Vrms
F	System frequency	60 Hz
Lf	Switching ripple filter inductance	5 mH
Cf	Switching ripple filter capacitance	2 μΗ
T_s	dSPACE Synchronous sampling	40 µH
	time	
G	Control gain for current harmonics	8 Ω
V_{DCref^*}	VSI DC bus voltage of the THSeAF	70 V
$\overline{PI_G}$	Proportional gain (K_p) , integral gain	0.025(4*)
	(K)	10(10*)

Table.2.Load Side Configuration parameter

Symbol	Definition	Value
Rnon-linear load	Load resistance	11.5 Ω
Lnon-linear load	Load inductance	20 mH
P_L	Linear load power	
PF	Linear load power factor	46 %
T_s	dSPACE Synchronous sampling	40 µH
	time	
fpwm	PWM frequency	5 kHz
V _{DCref*}	VSI DC bus voltage of the	70 V
	THSeAF	

2. PROPOSED THSeAF

The Transformer less Hybrid Series Active Filter (THSeAF) consists of an H-bridge converter connected in series between source and load. A shunt passive filter provides a low impedance for current harmonics [6]. A DC auxiliary source is connected to inject voltage during voltage sags. This grid is developed for a 2.2 KVA rated power. The proposed system parameters are shown in a Table.1 and Table.2. A source of 120Vrms is connected to 1.1KVA nonlinear load, 998VA linear load and 0.46PF [7]. The THSeAF is connected in series to inject the voltage during the time of distortion. On DC side the compensator (H-bridge) auxiliary source is connected. HSeAF is mostly used for compensating distortions in current type of non-linear loads. For example the distorted voltage and current harmonics are shown in Fig.2 during regular operation and when distortion is occurred due to non-linear loads.

Table.3. Single phase comparison of the THSeAF to existing SeAF [2]

Definition Proposed THSeAF						
Injection	non	2 per	1 per	1 per		
transformer		phase	phase	phase		
# of	4	8	4	4		
Semiconductor						
devices						
# of DC link	1+Aux.	1	2	1+Aux.		
storage	Pow.			Pow.		
elements						
AF rating to the	10-30%	10-	10-	10-30%		
load power		30%	30%			
Size and weight	The	High	Good	Good		
regarding the	lowest	_				
transformer,						
power						
switching drive						
circuit, heat						
sinks, etc.						
Industrial	The	High	Low	Low		
production	lowest	_				
costs						
Power loses	Low	Better	Low	Low		
including						
switching,						
conducting and						
fixed losses						
Reliability	Good	Low	Good	Good		
regarding						
independent						
operation						
capability						
Harmonic	Good	Good	Good	Low		
correction of						
current source						
load						
Voltage	Good	Better	Good	Good		
Harmonic						
correction at						
load terminal						
Power factor	Yes	Yes	Yes	No		
correction						
Power injection	Yes	No	No	Yes		
to the grid						

This proposed configuration can directly connect to the grid with isolating the bulky transformer makes the topology capable of controlling current harmonics, voltage distortion at the point of common coupling PCC [8]. Even if the number of switches are increased in the system. This THSeAF very effective and less costly compared to any series compensators, which have to connect the transformer to inject voltage into the power grid to compensate voltage distortion [9]. The passive filters consist of high pass filters [10]. A comparison between existing system and the proposed system is given in Table.3 . The advantages of proposed topology over existing are clearly stated.



Fig. 2. Regular operation and grid's voltage distortion

To understand the comparison table an equivalent single phase of each configuration as shown in Table.3. Estimation of production in financial is 45% reduction in components cost and also reduction in assembly terms.

2.1 Equivalent circuit configuration for current harmonics

The THSeAF acts as a VSI. To prevent the source from current harmonics which drift from the non-linear load, SeAF should provide low impedance for fundamental components and high impedance for all harmonics as indicated in Fig.3

The passive filter is mandatory to compensate current issues and voltage distortion and to maintain balanced voltages at load terminal. The performance of a SeAF for a current control is considered from Fig.3 of phasor equivalent circuit. The non-linear load is nothing but a resistance representing active power consumed and generated harmonic current impedance represent nonlinear load. If source current is free from harmonics that will improve the voltage distortion at the grid. The THSeAF acts as low impedance for harmonics and passive filters acts as low impedance for all harmonics and open circuit for fundamental. This also improves power factor.



Fig.3 THSeAF equivalent circuit for current harmonics^[1]

2.2 Control schemes of THSeAF and Proportional Integral

A DC auxiliary is connected to get a balanced supply at the load terminals. During the time of sags and swell DC auxiliary is used to inject or absorb power to keep the voltage magnitude at a particular limit at the load terminal. But still, if the compensation of voltage sag and swell is less imperative, a capacitor can be installed. Then, the DC link voltage across the capacitor will regulate as shown in Fig.4

An outer loop controller is used where a capacitor can replace an DC auxiliary source. An inner loop control strategy is indirect control principle. Gain represents as the impedance for current harmonics in a suitable level to clean the grid from harmonics which are fed from non-linear loads.



Fig.4 control system scheme of the active part

The second Proportional Integral (PI) controller in the outer loop is to enhance the effectiveness when regulating the DC bus. Thus, more accurate and faster response will be achieved without compromising the compression performance of the system. The gain should be kept in such a way that the current harmonics are prevented from flowing into the grid. As discussed for more accurate compensation of current harmonics, the voltage harmonics must be taken into consideration. The compensation of voltage is done to compensate the current harmonics is shown in Fig.5



Fig.5 block diagram of THSeAF and PL controller

The voltage distortions are not desired at the load terminals, the voltage sags and swell are investigated in the inner loop. The closed loop equations help us to indirectly maintain voltage magnitude at load side must be equal to (V_L) within a specified limit.

The complete control scheme of the proposed system THSeAF is shown in Fig.6 is implemented in MATLAB/ Simulink for real time simulations and calculation of the compensating voltage, the source voltage and load voltage along with the source current is given as the input signal to the system indirect control increases the stability of the system. The difference between the source current and fundamental components gives the source current harmonics. Where Vdc reference is the voltage required to maintain the dc bus voltage constant.

A phase locked loop PLL is used to get the reference angular frequency. The current harmonics contain fundamental components are similar to the source voltage in order to correct the power factor. This current represents the reactive power of the load. Gain acts as resistance to the harmonics and converts current into a reactive voltage. The voltage which is generated from the loop will clean the source current from harmonics. After that, a reference signal is compared with the measured output voltage and applied to the PI controller to generate gate signals.



Fig.7. Equivalent circuit model of solar cell

2.3 Modelling of PV modules

The modelling of photovoltaic array panel can be applied from the mathematical classical in Eq.(1), which is consequent from the cell equivalent circuit where every cells are identical [22].

$$I_{PV} = N_p \cdot I_{PH} - N_p \cdot I_0 \left[e \left(\frac{q(V_{PV} + R_S I_{PV})}{AKTN_5} \right) - 1 \right) - N_p \frac{(V_{PV} + R_S \cdot I_{PV})}{R_{SH} \cdot N_5}$$
(1)

Where I_{pv} is the photovoltaic panel current, V_{pv} is the PV output voltage, R_S is the cell series resistance, q is the electron charge in 1.6×10^{-19} C, R_{sh} is the cell parallel resistance, I_{ph} is the light produced current , k is the Boltzmann constant generally 1.38×10^{-23} J/K, A is dimensionless factor, I_O is the reverse saturation, T is the temperature in K, N_S and N_P represent number of cells jointed in series and parallel respectively. Generally, every PV cell is basically formed as a P-N junction. It converts sunlight energy into electrical energy without any interrupt of the environmental issues. The Equivalent mathematical model of PV system is shown Fig.7



When recommending a Maximum Power Point Tracking (MPPT), the most important job is to select and design a efficient converter, which is supposed to operate as the main part of the MPPT [11]. The DC to DC boost converter provides a positive polarity regulated output voltage with respect to the input voltage is shown. The boost converter is used to increases the output voltage of converter. This converter desires one inductor, switch and diode [12]. The boost converter output voltage is always higher than the input voltage magnitude. Therefore, this boost converter generally connects high load or battery voltages. Normally the boost converter cannot reflect impedances that are larger than load impedance value. So the boost converter does not achieve values near a PV modules open circuit voltage. That means Rload is always less than equal to Rmpp. The Maximum power point tracking will be tracked as if it is restricted to within the operating region.

For improving the efficacy of the output power of the photovoltaic panel and the inverter a MPPT with control algorithm are introduced. The control algorithm is necessary because photovoltaic panel characteristics have a non linear current vs. voltage with a unique point where the power produced is higher. This point depends on the temperature and irradiation of the situation. These conditions are change during the day and season of the year. So it is necessary to track the maximum power point accurately under all possible situations using MPPT approach. In this paper Perturb and Observe (P&O) method used to track the maximum power point. The P&O method operate as a perturbation in the functioning voltage of the DC link between the photovoltaic array boost converters. The perturbing the duty cycle of the converter implies modifying the voltage of the DC link of the power converter. The algorithm function based on the sign of the last perturbation and the sign of the last increment in the power are used to decide what the next perturbation should be.

3. PROPOSED RESULTS AND DISCUSSIONS

The proposed transformer less HSeAF is simulated in MATLAB as shown in Fig.8. In Fig.8a shows as full setup of the proposed schematic circuit diagram, which includes the input source of solar photovoltaic panel, controller by the famous Perturb and Observe method. Theses DC source as input of H bridge inverter, which is used to convert renewable DC input into AC power supply through the passive filter. The passive filter uses to reduces the low impedance path for current harmonics and inject power during voltage sags. The transformer less series active filter is connected to inject the compensating voltage to the transmission line. Further in Fig.8b shows as solar panel and PO development diagram. A single phase non-linear load with a rated power of 2KVA with 0.74 lag power factor is applied to Simulation [9]. A 2KVA, 123Vrms 60Hz variable source is used [10]. The THSeAF connected in series to the system to compensate the current harmonics and voltage distortions. A gain G=8 ohms equivalent 1.9P.U by referring is to control current harmonics. The capability to operate the system with low DC voltage is the main advantage of the proposed system[10].

DC auxiliary is given as 130Vdc. During voltage distortion the compensator controls the current harmonics, voltage magnitude at load and corrects power factor [11]. The simulated results of THSeAF are shown in Fig.9. which shows the improvement of source current [12]. The Fig.9 included the source voltage and source current, load voltage and load current, active filter voltage compressor and harmonics current of the passive filter. Its reflect the output of transformer less series active filter effectiveness. The grid is now balanced with protecting the system from current harmonics with a unity power factor operation [13]. Since the series controlled source prevents current harmonics, the source current is forced to be in phase with source voltage.

Fig.9 waveforms of THSeAF in set up diagram a) voltage source b) current source c) load voltage d) load current e) active filter voltage compressor f) harmonic current passive filter.



Fig.10 waveforms of PV panel setup diagram a) Panel voltage (Vpv) b) Panel current (Ipv) c) Panel power (Ppv) d)output power (Po)

The series compensator has the capability to regulate the load voltage so that the power factor becomes unity. The series compensator is used to control the power flow between two point of common coupling's PCC [13]. Fig.10 includes the solar photovoltaic panel setup of voltage, current, power and output waveforms [14][15]. Which shows the linear starting and maintain constant current and small spike voltage due to changes of environment conditions[16]. Fig.11 displays the comparison of renewable photovoltaic fed H-bridge inverter and constant DC source input to the inverter. The PV fed inverter shows

less oscillation compare to the constant DC source system.



Fig.11 comparison of between PV fed input source inverter and constant DC source fed inverter a) Output voltage of inverter b) Output current of inverter(Yellow colour represent- PV fed inverter output waveform, Pink represent- constant DC source fed inverter output waveform)

4. CONCLUSION

The purpose of this proposed system is to improve the power quality of the system by compensating harmonic currents and to improve and regulate the PCC voltage. By connecting the auxiliary source it can counteract the power flow in the system for constant power supply, when critical loads are connected. The configuration was simulated and validated. This active compensator provides constant and distortion free supply to the load terminals and also it eliminates the source harmonic current and improves power quality of the grid by isolating the transformer.

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