

# DESIGN OF 915MHz 3dB MINIATURIZED POWER DIVIDER FOR WIRELESS SENSOR NETWORK

First G. KALPANADEVI    Second Dr.S.RAVIMARAN

M.A.M. College of Engineering, Siruganur, Trichy, 621105.  
[gkdmance@gmail.com](mailto:gkdmance@gmail.com), 9443780910.

**Abstract:** The power divider for wireless sensor network (WSN) is presented. WSN plays a vital role in many applications like Internet of Things (IoT), big data analytics and data mining etc., More number of sensors are used in WSN to collect the data. Power divider is used to divide the power for each sensor placed in wireless nodes. It is proposed to design of 3dB Wilkinson power divider to split the power equally for all the nodes of WSN. To overcome the size reduction challenges of WSN, fractal technique is used in proposed design. Simulation tool, advanced design system (ADS) is used for simulation. The proposed Wilkinson power divider has the reduced size of 54 percentage than conventional Wilkinson power divider. The planned style has easy structure, better isolation loss and good insertion loss.

**Key words:** Fractal structures, Power divider, Wireless Sensor Network.

## 1. Introduction

Physical conditions like temperature, humidity, sound, wind etc., are monitored by sensors and data are recorded in central location. Data are transmitted wirelessly. Each sensor has transceiver, microcontroller unit, antenna and battery. Antennas are used to transmit and receive the data from one sensor node to another node. Wireless Sensor Network (WSN) plays a important role in military applications as battlefield surveillance, industrial and consumer applications as industrial process monitoring and control, machine health monitoring and so on. Figure 1 shows the architecture of wireless sensor network.

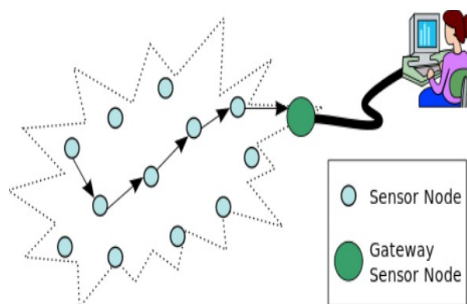


Fig 1. Architecture of wireless sensor network

Today's wireless communication system entails compact, high gain, and simple structure antennas to assure reliability and high efficiency characteristics. The key features of Microstrip technology draws researcher's attention and hence broadly used for both antenna and feed [8]. The limitations of microstrip antennas such as narrow bandwidth and low gain can be addresses with array of antennas [18]. The dissemination of voltages between the elements of an array depends on feeding network. Appropriate feeding network adds all of the induced voltages to feed into single point. Power distribution among antenna elements can be improved by corporate feed network. The phase change is introduced by beam steering in corporate feed network [16].

While composing associate antenna array, series or company feeding is also chosen. In the latter case, the feeding network requires power dividers in various topologies, such as Lange [2], Wilkinson [1] or a simple T-junction [10].

The Wilkinson power divider (WPD) is the robust power divider with all the output ports matched and only the reflected power is dissipated. The quarter-wave microstrip lines in the conventional WPD are substituted by eight-wave transmission line. The isolation loss of  $\lambda/8$  transmission line is address by introducing a capacitor between the output ports [20]. Farooq Razzaz et.al proposed an UWB Wilkinson power divider with tapered transmission lines to reduce the element length [19]. In [9], author discussed broadband Wilkinson balun using purely left handed transmission line for better return loss.

In past, miniaturization of power divider involve various methods viz., 3D techniques [5], planar artificial transmission lines [12], capacitive loading [9], stepped impedances [6], large inductance with transverse slits [4], with lumped parameters by replacing quarter wave section [7, 3], varactor tuning [14], open stub technology [15], and periodically loaded stubs [13] etc. It is observed that

the lumped element technique is dependent on the quality factor and self-resonant frequency of an inductor and the capacitive loading method reduces bandwidth and insertion loss [11]. However, to realize the standard performance whereas reducing the scale of the divider remains a tough task. In past techniques are wide applied to antenna style for the aim of size reduction. In [17], author addressed the miniaturization using fractal structure.

Wireless sensor network are used to sense and monitor the power, communicating and processing abilities to deliver the sensed information between various nodes present in the network. A network consists of many nodes and each node consists of an antenna to share the information among the networks.

Power divider is a passive and reciprocal microwave device which can allow the signal to pass in two directions. The power dividers are used to divide the power to various antennas in the network and vice-versa.

In this work, we proposed simple WPD utilizing fractal technique to reduce the size without degrading the performance. The paper is arranged as, Sect. 2 outlines the design of the proposed power divider, Sect. 3 discusses the results and performance of the power divider followed by conclusion.

## 2. Power Divider Structure and Design

In WPD, it is necessary to have two transitions from and back as characteristics impedance ( $Z_0$ ). The Wilkinson power divider provides better isolation by matching all the output ports properly and dissipates the reflected power.

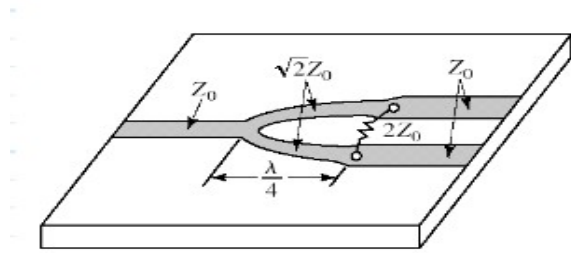


Fig. 2 Structure of Conventional Wilkinson power divider

The geometry of the Wilkinson power divider is shown in Figure 2. The Wilkinson power divider can also be used to provide arbitrary power division. The  $Z_0$  transmission line is divided into two transmission lines of impedance  $2\sqrt{Z_0}$  and vice

versa after quarter wave section from  $2\sqrt{Z_0}$  to  $Z_0$ . Initially, normal WPD is constructed with 50 ohm impedance. Keysight's Advanced Design System (ADS), electronic design automation software system is used to design and simulate the structure. For the dimensions of the microstrip line, the characteristics impedance can be calculated as

$$Z_0 = \begin{cases} \frac{60}{\sqrt{\epsilon_r}} \ln \left( \frac{8D}{W} + \frac{W}{4D} \right) & \text{for } \frac{W}{D} \leq 1 \\ \frac{120\pi}{\sqrt{\epsilon_r} [W/D + 1.393 + 0.667 \ln(W/D + 1.444)]} & \text{for } \frac{W}{D} \geq 1 \end{cases} \quad (3)$$

The characteristic impedance ( $Z_0$ ) of a microstrip line is also related to the conductor width and dielectric thickness. The  $Z_0$  of the microstrip line is determined by equation (1). More conveniently, the ratio of conductor width to dielectric thickness can be determined by equation (2) for given characteristic impedance and its relative permittivity by the following equations.

$$\frac{W}{D} = \begin{cases} \frac{0.8e^A}{e^{2A} - 2} & \text{for } \frac{W}{D} < 2 \\ \frac{2}{\pi} \left[ B - 1 - \ln(2B - 1) + \frac{\epsilon_r - 1}{2\epsilon_r} \left( \ln(B - 1) + 0.39 - \frac{0.61}{\epsilon_r} \right) \right] & \text{for } \frac{W}{D} > 2 \end{cases} \quad (2)$$

Where

$$A = \frac{Z_0}{60} \sqrt{\frac{\epsilon_r + 1}{2}} + \frac{\epsilon_r - 1}{\epsilon_r + 1} \left( 0.23 + \frac{0.11}{\epsilon_r} \right) \quad (3)$$

$$B = \frac{377\pi}{2Z_0\sqrt{\epsilon_r}} \quad (4)$$

For the design of constructing the width of the microstrip line is calculated by the design equation (5). From that equation the calculated value is the ratio of conductor width to the dielectric thickness followed the equation (2).

$$W = \text{calculated value} * D \quad (5)$$

The length of microstrip line is intended by equation (6)

$$l = \frac{90^\circ (\pi/180^\circ)}{\sqrt{\epsilon_r} K_0} \quad (6)$$

where  $K_0 = \frac{2\pi f}{c}$  is the value which relates the length and the resonant frequency of the power divider.

Figure 3 shows the geometry of the designed WPD with dimensions. The dimension of the designed WPD operates at 915 MHz is 118.62mm x 60.45 mm.

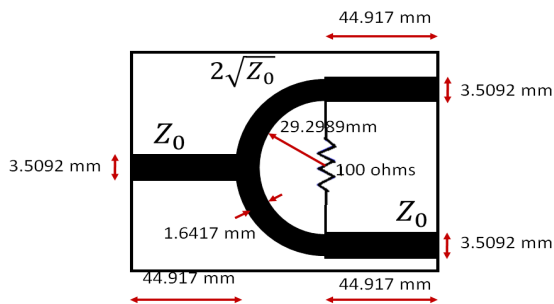


Fig.3 Geometry of Wilkinson Power Divider.

To reduce the size of the design a fractal structure is proposed. A form could be a self-similar style to maximize the length, or increase the perimeter (on inside sections or the outer structure), of material that can receive or transmit electromagnetic radiation inside a given total extent or volume. Such fractals are also referred to as multilevel and [space filling curves](#), but the key aspect lies in their repetition of a motif over two or more scale sizes, or "iterations". For this reason, shape based mostly devices area unit terribly compact, multiband or band, and have helpful applications in wireless telephone and microwave communications. Fractal style is capable of in operation with good-to-excellent performance at many alternative frequencies at the same time. To miniaturize the size of the conventional design, fractal pattern is proposed with single iteration. With this FWP, the dimension has reduced to 55.15mm x 56.48mm. Figure 4 shows the geometry of FWP with its dimensions.

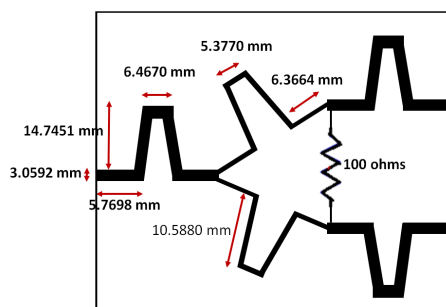
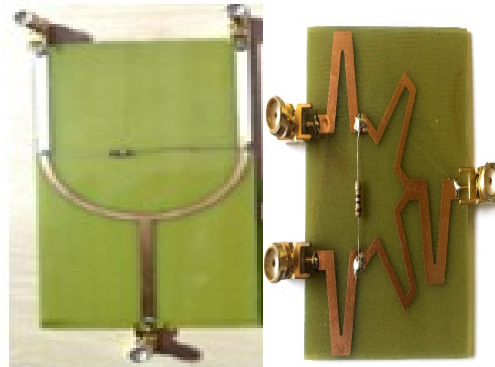


Fig.4 Geometry of proposed FPWD.

Both the general WPD and proposed FWP are fabricated on FR4 substrate with dielectric constant

4.4 and a thickness of 1.6mm. Figure 5 shows the fabricated WPD and FWP.



(a) (b)

Fig.5 Photographs of Prototype WPD.

A two way Wilkinson power divider has been designed for the frequency of 915MHz by using fractal techniques to reduce the size of the conventional WPD.

### 1. Results and Discussion

In the Power divider design return loss, insertion loss and isolation loss are the essential parameters. Hence, we mainly motivated on analyzing the mentioned characteristic. A Vector Network Analyzer of (ZVH4) of Rohde &Schwartz is utilized for validating the measured results.



Fig.6. Experimental setup of the proposed conventional WPD and fractal WPD.

Experimental setup of the proposed conventional Wilkinson power divider and fractal WPD are shown in Fig. 6.

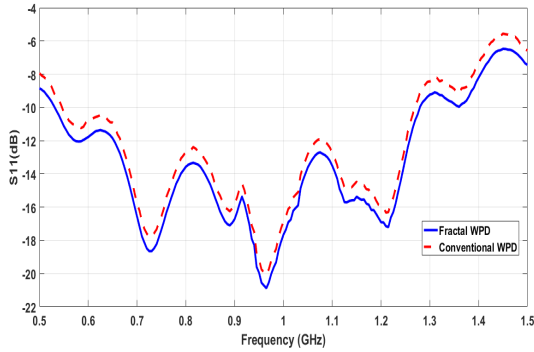


Fig 6. Input Return loss.

Figure 6 shows the input return loss of fractal WPD and normal WPD and shows that it has greater than 20dB for the operating frequency of 915MHz. It is observed that, there is a good agreement between fractal WPD and normal WPD.

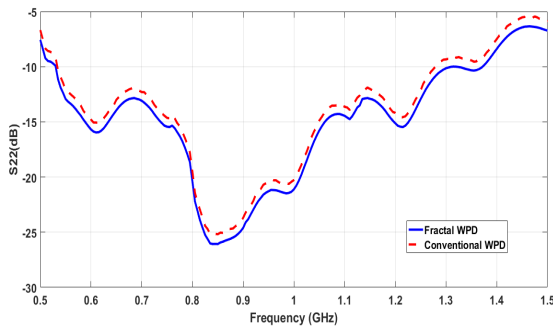


Fig 7. Output Return loss.

Figure 7 shows the output return loss of fractal WPD and normal WPD and shows that it has greater than 25dB for the operating frequency of 915MHz. Wilkinson power divider realized using fractal technique achieves the good return loss at all the ports.

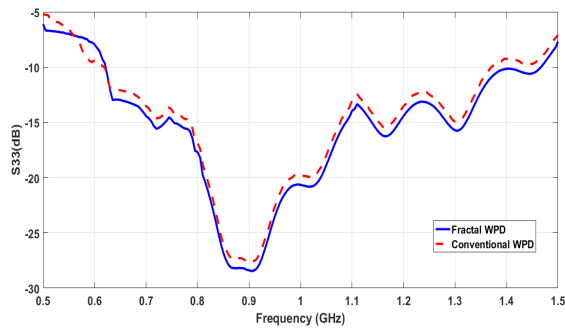


Fig 8. Output Return loss.

Figure 8 shows the output return loss of fractal WPD and normal WPD and shows that it has greater than 25dB for the operating frequency of 915MHz. Return loss at the two output ports 2 and 3 are equal and shown in figure 5 and figure 6 respectively which represents the proposed power divider has the equal power division at the two output ports.

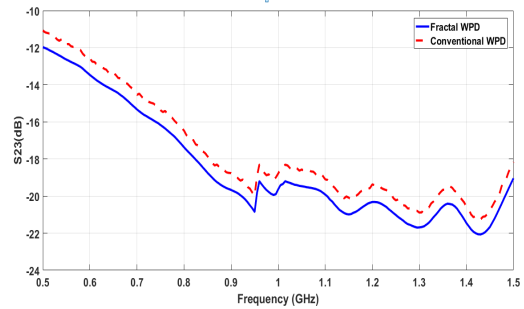


Fig 9. Isolation loss.

Figure 9 shows the isolation loss of fractal WPD and normal WPD and shows that it has greater than 20dB for the operating frequency of 915MHz. It shows that it has better isolation between two output ports.

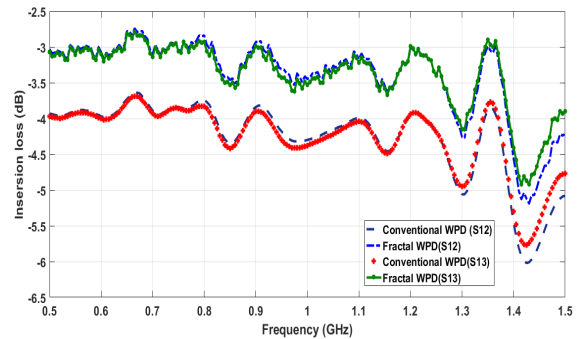


Fig 10. Insertion loss.

Insertion loss of fractal WPD and normal WPD are shown in Figure 10. It shows that it has 3dB insertion loss between port 1 & 2 and also between port 1 & 3. Figure 10 shows that there is a equal power division between port 1 and 2 and between port 1 and 3 for the operating frequency of 915MHz. Table 1 summarizes the performances of designed power dividers.

Table. 1 Performance Comparison of WPD and FWPD

Parameters	WPD	FWPD
Operating	915	915

Frequency (MHz)			
Input	return	21	20
loss(dB)			
Output	return	28.6	27.5
loss			
Isolation loss		20.5	19.8
Insertion loss		3.4	3.5
Dimension		118.62mm	55.15mm x
		x 60.45 mm	56.48mm

The performance characteristics of WPD and FWPD are compared in Table 1. Fractal based WPD achieves a good result with ideal values of WPD with reduced size without degrading the performance. The proposed design has simple structure, better isolation loss, return loss and good insertion loss.

#### 4. Conclusion

The design of a miniaturized fractal based Wilkinson power divider (FWPD) addressing GSM band has been presented. The proposed structure has a dimension of 5.648cm x 5.515cm where conventional WPD has a dimension of 11.862cm x 6.045cm. With the proposed fractal structure achieved 54% size reduction. The results show that with the FPWD, desired isolation and insertion loss are achieved. Simple and compact FWPD is a good candidate for drive system on substrate (SoS).

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